• General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

• Features

■AEC-Q101 Qualified

■Low $R_{DS(ON)}$ to minimize conductive loss

■Low Gate Charge for fast switching

■Low Thermal resistance

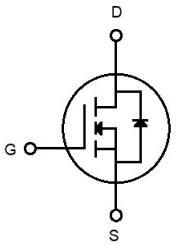
•Application

■BLDC Motor driver

■DC-DC

■Load Switch

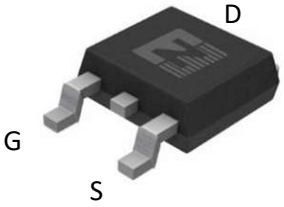
• Product Summary



V_{DS}= 60V

$R_{DS(ON)}$ = 5.2mΩ

I_D= 68A



TO-252

RoHS

REACH COMPLIANT

HF

•Ordering Information:

Part NO.	ZMSA052N06HD
Marking	ZMS052N06H
Packing Information	REEL TAPE
Basic ordering unit (pcs)	2500

•Absolute Maximum Ratings (T_C=25℃)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}		60	V
Gate-Source Voltage①	V_{GS}		±20	V
Continuous Drain Current	I_D	T_C=25℃	68	A
	I_D	T_C=75℃	61	A
	I_D	T_C=100℃	52	A
Pulsed Drain Current	I_{DM}	Pulsed; t_p ≤ 10 μs; T_{mb} = 25 °C;	272	A
Total Power Dissipation	P_D	T_C=25℃	75	W
Total Power Dissipation	P_D	T_A=25℃	2.4	W
Operating Junction Temperature	T_J		-55 to +175	℃
Storage Temperature	T_{STG}		-55 to +175	℃
Single Pulse Avalanche Energy	E_{AS}	L=0.1mH, V_{GS}=10V, Rg=25Ω,	61	mJ
		L=0.5mH, V_{GS}=10V, Rg=25Ω,	128	mJ
ESD Level (HBM)	CLASS 2			

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}		-	2	°C/W
Thermal resistance, junction-ambient②	R_{thJA}		-	62	°C/W
Soldering temperature	Tsold		-	260	°C

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V, I_D =250uA	60			V
Gate Threshold Voltage	$V_{GS(TH)}$	V_{GS} =V_{DS}, I_D =250uA	2	2.7	4	V
Drain-Source Leakage Current	I_{DSS}	V_{GS}=0V, V_{DS}= 60V			1.0	uA
Gate- Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS} = 0V			100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D= 20A		5.2	6.5	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V, I_{SD} = 10A		16		s
Diode Forward Voltage	V_{FSD}	V_{GS} =0V, I_{SD} = 20A			1.3	V

•Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	Ciss	f = 1MHz, V_{DS}=25V	-	1720	-	pF
Output capacitance	Coss		-	960	-	
Reverse transfer capacitance	Crss		-	91	-	
Gate Risistance	Rg	f = 1MHz	-	1.4		Ω
Total gate charge	Qg	V_{DD} = 15V, I_D = 20A, V_{GS} = 10V	-	29	-	nC
Gate - Source charge	Qgs		-	4.4	-	
Gate - Drain charge	Qgd		-	7.3	-	
Turn-ON Delay time	$t_{D(on)}$	V_{GS}=10V,V_{DS}=15V, R_G =3.3Ω, I_D =20A	-	16	-	ns
Turn-ON Rise time	t_r		-	17	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	28	-	ns
Turn-Off Fall time	t_f		-	15	-	ns
Reverse Recovery Time	t_{RR}	V_{DD}=20V, dI_S/dt = 100A/us, I_S=20A	-	21	-	ns
Reverse Recovery Charge	Q_{RR}		-	20	-	nC

Fig.1 Gate-Charge Characteristics

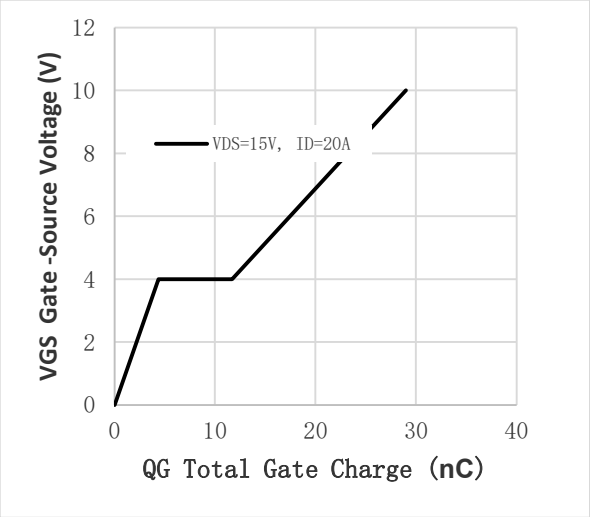


Fig.2 Capacitance Characteristics

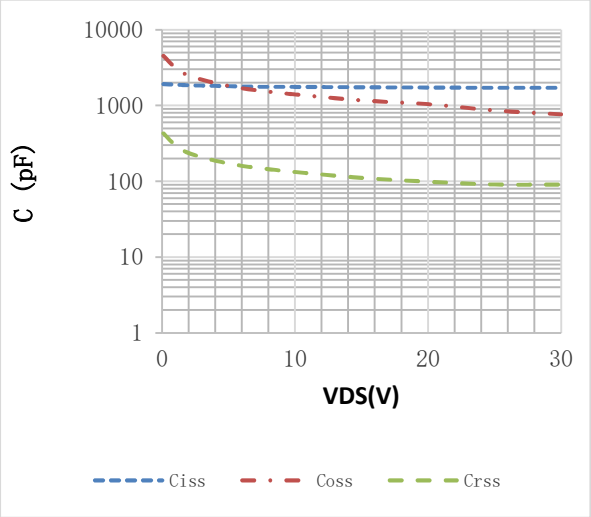


Fig.3 Power Dissipation

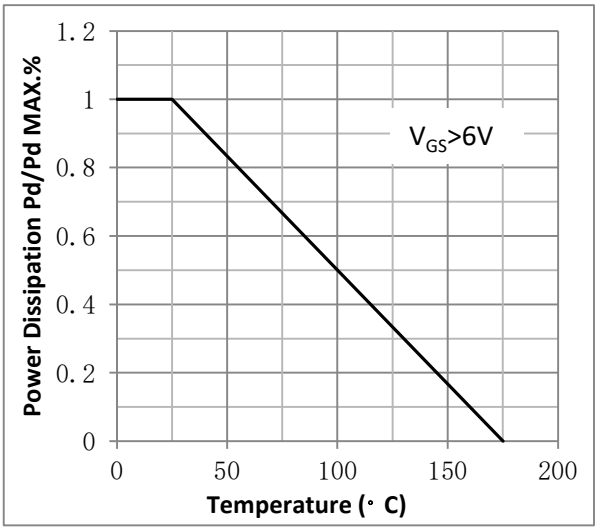


Fig.4 Typical output Characteristics

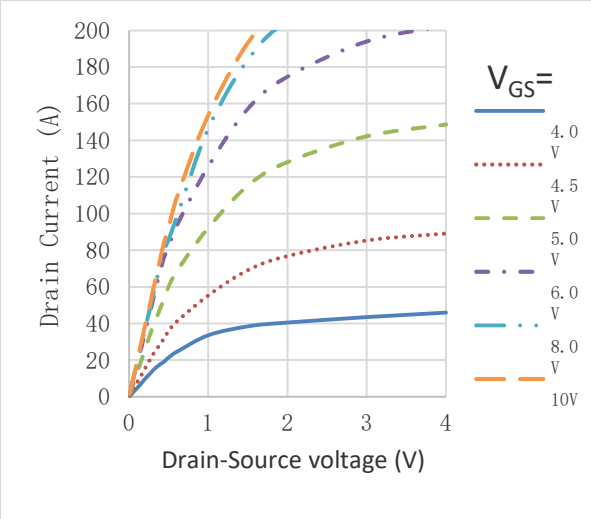


Fig.5 Threshold Voltage V.S Junction Temperature

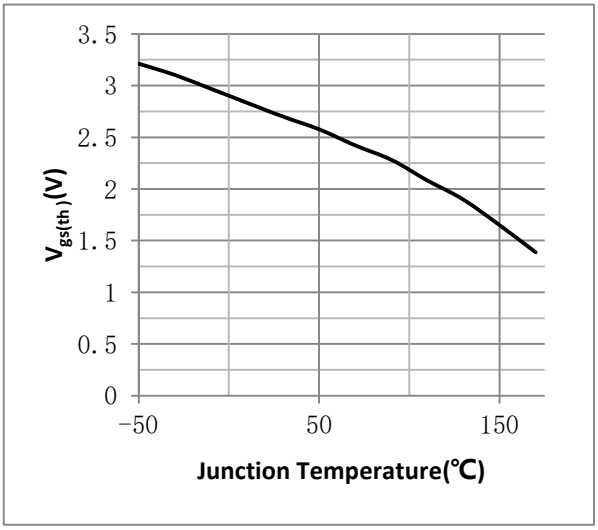


Fig.6 Resistance V.S Drain Current

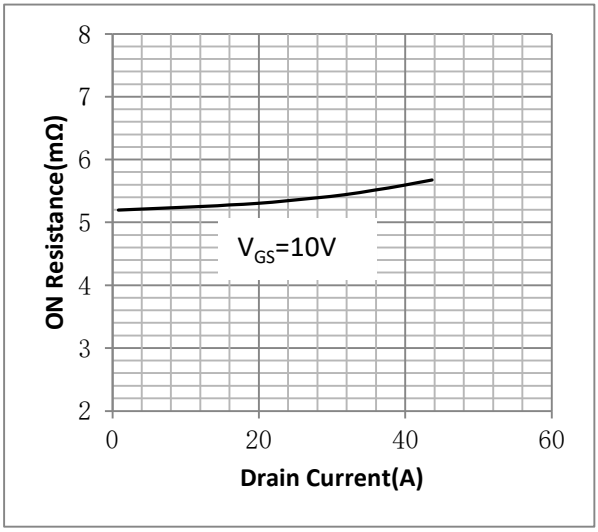


Fig.7 On-Resistance VS Gate Source Voltage

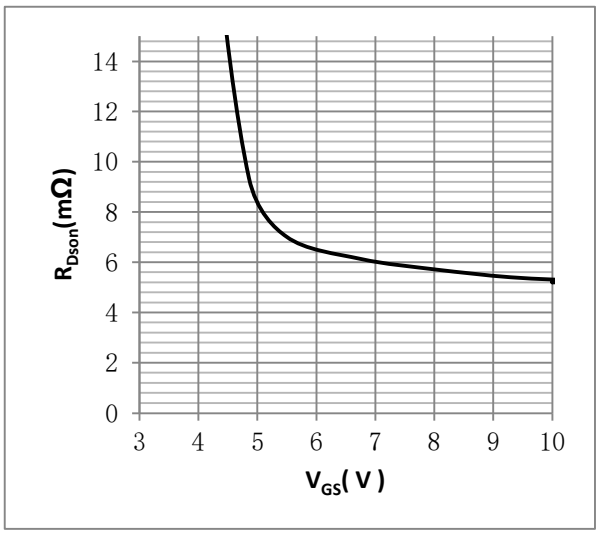


Fig.8 On-Resistance V.S Junction Temperature

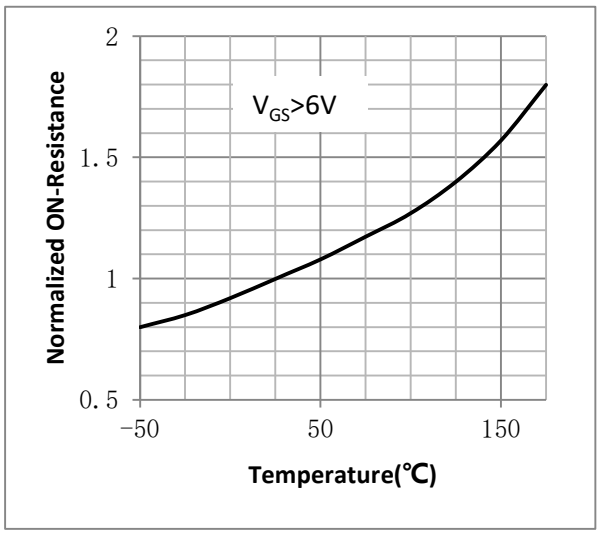


Figure 9. Diode Forward Voltage vs. Current

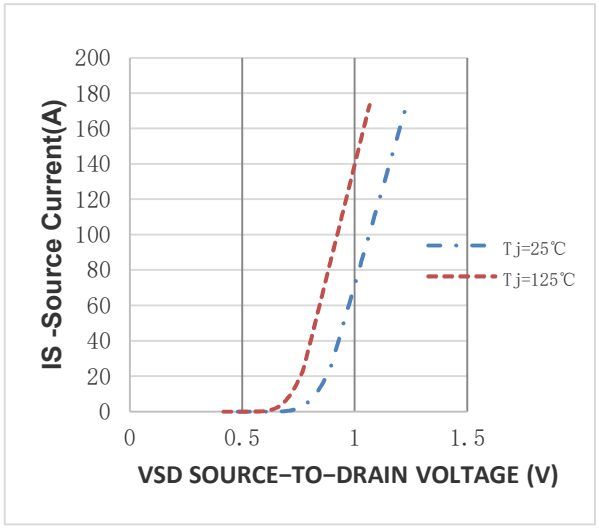


Figure 10. Transfer Characteristics

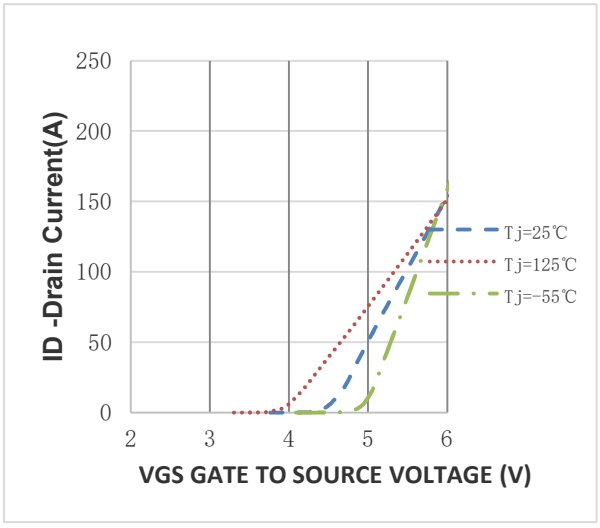


Fig.11 Safe Operating Area

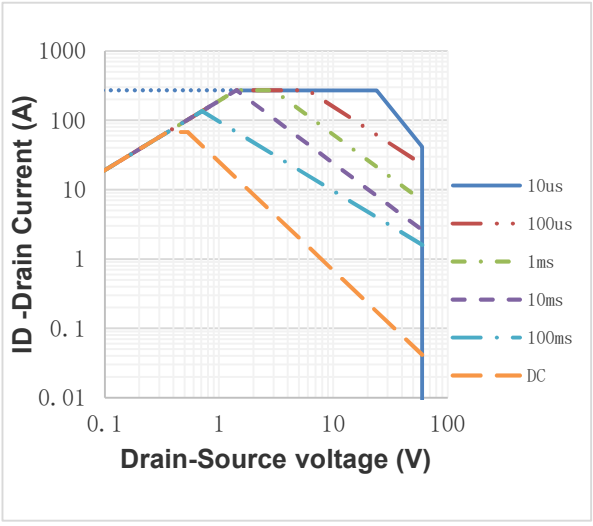
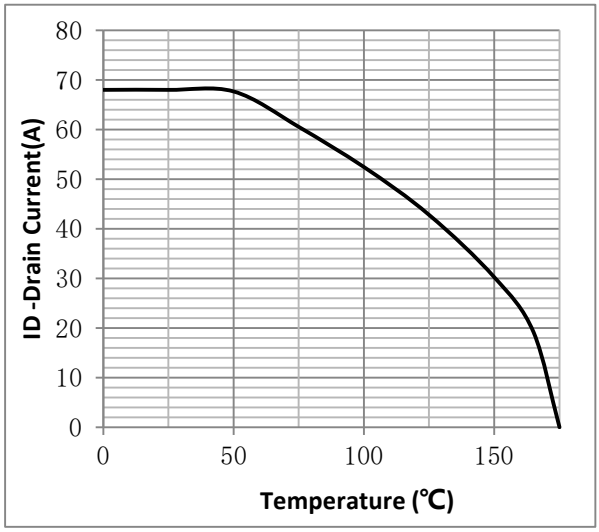
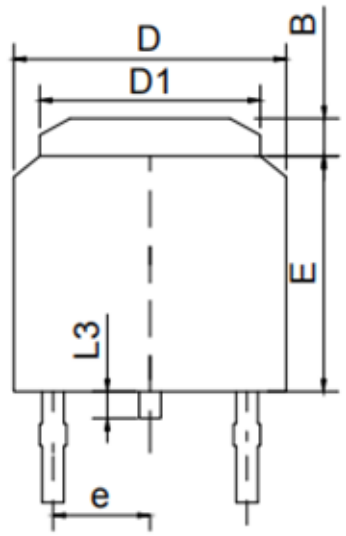


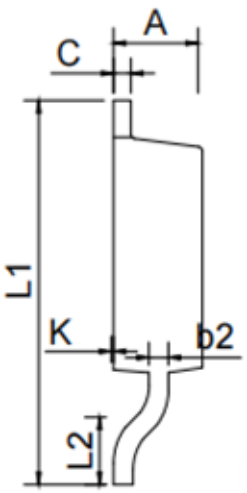
Fig.12 ID vs. Junction Temperature③

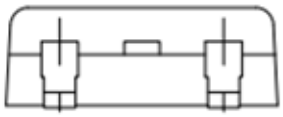


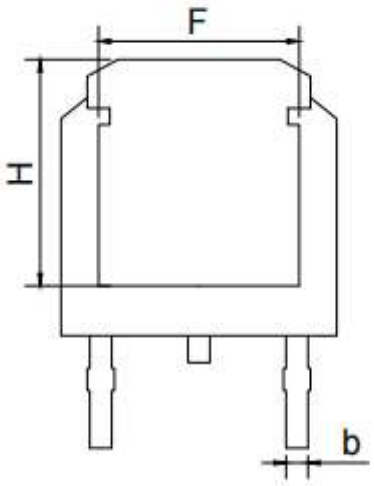
●TO-252 Package Outline

1	WIPOD004	TO-252 PACKAGE OUTLINE SPEC
REV	DRAWING NUMBER	TITLE

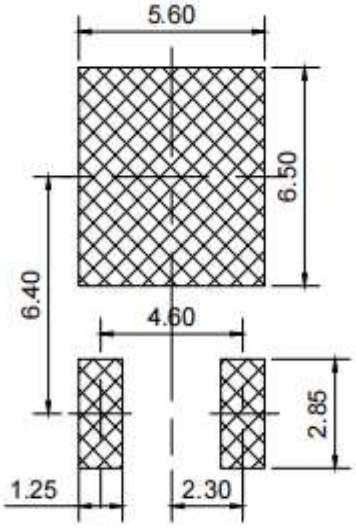








Land Pattern
(Only for Reference)



SYMBOLS	COMMON	
	UNIT: mm	
	MIN.	MAX.
A	2.18	2.40
B	0.89	1.27
b	0.50	0.89
b2	0.45	0.61
C	0.45	0.61
D	6.35	6.75
D1	4.95	5.50
e	2.24	2.35
L1	9.40	10.41
L2	1.25	1.78
L3	0.60	0.90
E	5.95	6.25
F	4.32	4.95
H	4.90	5.30
K	0.00	0.13

Note:

① Pulse : VGS=+20V/-20V, Duty cycle=50%,Tj=175℃, t=1000 hours; For DC， the following test conditions can be passed: VGS=+20V/-10V, Tj=175℃, t=1000 hours；

② Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate；

③Practically the current will be limited by PCB, thermal design and operating temperature. VGS=10V.

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Revision History

Version	Date	Change
A	2022.10.10	
B	2023.3.25	1.Add Reach,HF figure,2.ID modify
C	2024.2.28	Correct package outline dimension
D	2024.6.11	Correct marking information